

Course Material Submission Form

OAN Match Definition Form

Today's Date: 10/11/07

Use this table to specify institutional data	
College/University:	Youngstown State University
Name and title of individual submitting on behalf of the college/university	
Name:	Tony Messuri
Title:	Coordinator, Electrical Engineering Technology
Address:	1 University Plaza
Email:	apmessuri@ysu.edu
Phone:	330.941.3754
Fax:	330.941.2386

Indicate the reason for this submission:

New Course Match
 Revised Materials - Faculty review panel requested clarification
 Revised Materials - Institution submitting additional information
 Revised Materials - Course content revised by institution, including situations of both content and credit hour change
 Revised Materials - Other

Describe specific revisions being made for "Revised Materials" submissions:

Institutional Notes to Faculty Panel (the institution is encouraged to add any additional clarifications for this submission):

Table 1 – Use this table to describe the course match for which materials are being submitted for the first time or revised.

Proposed effective year and term of match (Final effective date will depend on actual approval of match by faculty panel. Effective Year and Term is the first term in which students taking the course will receive matching credit.)

Semester institutions complete this row:
 2008 Academic Year Summer Autumn Spring

Quarter institutions complete this row:
 20 Academic Year Summer Autumn Winter Spring

Ohio Articulation	OET 003
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Number (OAN) (Use a separate form for each OAN.):	
Number of courses in the match:	2 (up to 10)
Current status of match:	<input checked="" type="checkbox"/> First time submission <input type="checkbox"/> Approved <input type="checkbox"/> Submitted <input type="checkbox"/> Disapproved <input type="checkbox"/> Error <input type="checkbox"/> Resubmitted <input type="checkbox"/> Pending <input type="checkbox"/> Error with enrollment <input type="checkbox"/> Not submitted
Course or Courses being matched to or currently matched to the OAN listed above. (Course Numbers must be exactly what will appear on a student's transcript.):	Course Number
	1. EET 1502 Circuit Theory 2
	2. EET 1502L Circuit Theory 2 Laboratory
	3.
	4.
	5.
	6.
	7.
	8.
	9.
10.	

Table 2 - Use this table to submit course materials for the first time or to revise previously submitted course materials. You must submit each course in a separate form, repeating the match definition information in Table 1 above for each form submitted.

Course Number. (Course Numbers must be exactly what will appear on a student's transcript.):	EET 1502L	Course Title:	Circuit Theory 2 laboratory
Hours (be sure that the hours for this course matches the hours in the OAN.)			
<input checked="" type="checkbox"/> Semester Hours		<input type="checkbox"/> Quarter Hours	
Total Credit Hours	1	Lecture Hours	0
		Laboratory Hours (if applicable)	3
Course Placement in Major:		<input checked="" type="checkbox"/> Major Requirement <input type="checkbox"/> Major Elective <input type="checkbox"/> Other	
Pre-Requisite Course work (if applicable) (Be sure this is consistent with the OAN definition): C or better for EET 1501, EET 1501L, Concurrent MATH 1513 Algebraic & Transcendental Functions, Concurrent with EET 1502			
Catalog/Course Description: Measure effective values of AC Currents and voltages, observe waveforms with oscilloscopes, verify impedance concepts and phasor diagrams for AC series/parallel networks and resonance circuits. Computer circuit analysis with Pspice.			
Texts/Outside Readings/Ancillary Materials (Be sure that the text meets performance expectations): "Introductory Circuit Analysis" 11 th ed. By Robert L. Boylestad			

Course Objectives and/or Plan of Work:

1. Sinusoidal wave properties
2. Complex numbers and phasors
3. Behavior of transformers
4. Steady state behavior of RC circuits under AC conditions
5. Steady state behavior of RL circuits under AC conditions
6. Steady state behavior of RLC circuits under AC conditions
7. Analysis of basic filter circuits
8. AC network theorems such as superposition, Thevenin's and Norton's theorems
9. * Three phase and/or poly-phase systems
- 10.* Power factor analysis

Description of Assessment and/or Evaluation of Student Learning (The

assessment plan needs to be appropriate for the expected rigor of the course) :

Experiments and Laboratory Reports 100%

Master Syllabi and Working Syllabi (if both are used):

YOUNGSTOWN STATE UNIVERSITY
Electrical Engineering Technology
Course Policy Statement

Student Responsibilities:

The student is responsible for all course material described in the syllabus.

Grading:

The final course grade is based on the following weighted averages:

Experiments and Laboratory reports100%

The final course grade will be determined by the following scale:

90 – 100	A
80 - 89	B
70 - 79	C
60 - 69	D
0 - 59	F

The instructor reserves the right to lower these standards as he deems appropriate. The final term grade shall be strengthened whenever the student:

1. Demonstrates continuous improvement in performance in written examinations.
2. Contributes to lecture activity through verbal response or blackboard exhibition.
Students' viewpoints are welcome.

Exercises:

The student is required to read all of the sections listed on the syllabus and to complete written homework problems assigned. The assigned exercises are considered to be the minimum requirement and the student is encouraged to complete additional exercises and attempt unassigned laboratory experiments whenever possible.

Attendance:

Class attendance is not mandatory but is strongly encouraged. The student assumes full responsibility for all course material, class assignments, and exams as given or scheduled.

Office Hours:

Office hours are available to all students for the purposes of advisement, consultation, and receiving assistance with course assignments.

M – 14:15–16:45

W – 14:15-15:45

TH – 12:00-13:00

1502L. *Circuit Theory 2 Lab*. Measure effective values of AC currents and voltages, observe waveforms with oscilloscopes, verify impedance concepts and phasor diagrams for AC series/parallel networks and resonance circuits. Computer circuit analysis with PSPICE. Three hours per week. Concurrent with EET 1502. 1 s.h.

Though instructors are responsible for taking all reasonable precautions to prevent cheating and plagiarizing, students share a joint responsibility and should report any act or academic dishonesty to the instructor.

An instructor may give a failing grade and/or refer for disciplinary action any student who participates in acts of academic dishonesty. The failing grade may be either for the test or paper on which the cheating or plagiarism occurred or for the entire course. The circumstances of the incident should be discussed with the student prior to giving the failing grade or referral.

There will be no make-up exams except in extreme special cases where arrangements have been made with the instructor PRIOR to the scheduled exam. Laboratory experiments cannot be made up.

DISABILITY NOTICE

In accordance with University procedures, if you have a documented disability and require accommodations to obtain equal access in this course, please contact me privately to discuss your specific needs. You must be registered with the Disability Services Office, 3310 Beeghly Hall and provide a letter of accommodation to verify your eligibility. You can reach the Disability Services Office at 330/941-1372.

THE CENTER FOR STUDENT PROGRESS is a resource on Campus established to help students successfully complete their university experience. Please phone 330/941-3538 or visit the Center for help “working the system” for tutoring, or for individual assistance with academic and social success. The main Center is located in Kilcawley West under the bookstore. Disability Services is located in Wick House.

ELECTRICAL ENGINEERING TECHNOLOGY COURSE SYLLABUS FOR EET 1502L: CIRCUIT THEORY 2 Lab

EET 1502/L

Required Text: “Introductory Circuit Analysis” 11th ed. By Robert L. Boylestad

Week Experiments Topics

1. Introduction to Oscilloscope
2. Introduction to Electronic Work Bench – Multisim9 Circuit Simulations & Pspice
3. AC/DC Circuit Analysis
4. Electronic Work Bench (MultiSim9/PSpice) Simulations - AC Circuits
5. Transients Measurements – RL & RC Circuits
6. Transients Simulations – RL & RC Circuits
7. Series and Parallel AC Circuit Analysis - Measurements
8. Series and Parallel AC Circuit Analysis - Simulations
9. Electronic Work Bench (MultiSim9/PSpice) Simulations - AC Circuits
10. Phasor Diagrams – AC Circuits
11. Maximum Power Transfer – AC Circuits

Additional Documentation:

OBR Use

Approved-Effective Date	
Pending (i.e. Additional Information Requested)	
Disapproved	
Today's Date	